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ATTORNEY DOCKET NO.: S. CHITTIPEDDI 79

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT  
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SEP 25 2003  
TECHNOLOGY CENTER 2800

Applicant: Sailesh Chittipeddi  
Serial No.: 09/596,382  
Filed: June 16, 2000  
Title: A PROCESS FOR MANUFACTURING AN INTEGRATED  
CIRCUIT INCLUDING A DUAL-DAMASCENE STRUCTURE  
AND A CAPACITOR  
Grp./A.U.: 2812  
Examiner: H. Jey Tsai

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on  
09/16/2003 (Date)  
Stephanie Pritt  
(Printed or typed name of person signing the certificate)  
Stephanie Pritt  
(Signature of the person signing the certificate)

Sir:

**REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. §1.111**

In response to the Examiner's Action mailed June 25, 2003, please reconsider the above-identified application in view of the following remarks.

**REMARKS/ARGUMENTS**

The Applicant has carefully considered this application in connection with the Examiner's Action and respectfully requests reconsideration of this application in view of the following remarks. The Applicant originally submitted Claims 1-22 in the application. The Applicant previously elected to pursue Claims 1-12 and canceled Claims 13-22 without prejudice or disclaimer. The